

IN THE CLAIMS

Upon entry of the present amendment, the status of the claims will be as is shown below. This listing of claims replaces all previous versions and listings of claims in the present application.

Claims 1-10 (Canceled)

11. (Previously Presented) A video output apparatus, comprising:

a first video output source and a second video output source that each output a sync signal and a video signal corresponding to said sync signal;

a changeover controller that changes a sync signal and a video signal to supply to a display apparatus, from the sync signal and the video signal of the first video output source over to the sync signal and the video signal of the second video output source;

a reset signal generator that generates a reset signal synchronized with the sync signal of the first video output source, in accordance with the changeover in the changeover controller; and

a timing changer that inputs the reset signal to the second video output source to synchronize the sync signal of the second video output source with the sync signal of the first video output source.

12. (Currently Amended) The video output apparatus of claim 11, wherein the changeover controller turns off power-~~of~~ to the first video output source and turns on power-~~of~~ to the second video output source, in accordance with the changeover in the changeover controller.

13. (Previously Presented) The video output apparatus of claim 11, wherein the timing changer inputs the reset signal to the second video output source at a first timing at which the reset signal generator generates the reset signal and at a second timing that arrives every predetermined number of clock pulses with respect to the first timing.

14. (Previously Presented) The video output apparatus of claim 11, wherein said second video output source counts a number of reference clock pulses and outputs the sync signal when the counted number of reference clock pulses reaches a predetermined number

15. (Previously Presented) The video output apparatus of claim 11, wherein the second video output source resets a number of reference clock pulses when the timing changer inputs the reset signal to said second video output source.

16. (Previously Presented) A communications terminal apparatus equipped with the video output apparatus of claim 11.

Claims 17-18 (Cancelled)